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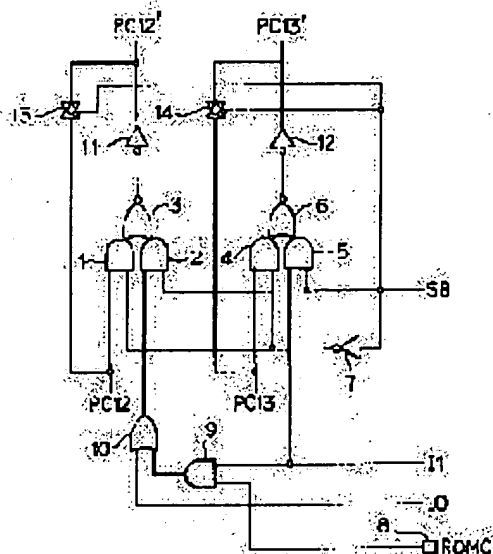
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(54) ADDRESS SPECIFYING CIRCUIT

(57)Abstract:

PURPOSE: To provide the address specifying circuit which accesses a mask ROM so that two kind of independent program data can be read out without any mask alteration of the mask ROM.

CONSTITUTION: If a low-level input signal is applied to a terminal (8) when an instruction instructing of a change to a 2nd storage area of the mask ROM is executed, the high-order bits PC13' and PC12' of address data are so set that the 2nd storage area can be accessed; when a high-level input signal is applied to the terminal (8), on the other hand, the high-order bits PC13' and PC12' of the address data are so set that a 3rd storage area can be accessed. Namely, a jump to the different area of the mask ROM can be made by varying the input signal level to the terminal 8 although the same program instruction is executed.



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